

What is claimed is:

1. An LCD comprising:

a plurality of signal lines formed at constant intervals;

a common electrode arranged in a vertical direction to the signal lines;

a plurality of first electrostatic protecting circuits arranged at one side of the common electrode between adjacent first and second signal lines in pairs to be connected with the first signal lines of the respective pairs and the common electrode; and

a plurality of second electrostatic protecting circuits arranged at the other side of the common electrode to be connected with the second signal lines of the respective pairs and the common electrode.

2. The LCD of claim 1, wherein the common electrode includes two first and second lines, the first line being connected with the first electrostatic protecting circuit and the second line being connected with the second electrostatic protecting circuit.

3. The LCD of claim 2, wherein the first and second electrostatic protecting circuits are arranged at both sides around the first and second lines.

4. An LCD comprising:

a plurality of signal lines formed at constant intervals;

a common electrode arranged in a vertical direction to the
signal lines;

a plurality of first electrostatic protecting circuits connected with odd numbered signal lines and the common electrode at one side of the common electrode; and

a plurality of second electrostatic protecting circuits connected with even numbered signal lines and the common electrode at the other side of the common electrode.

5. The LCD of claim 4, wherein the odd numbered signal lines and the even numbered signal lines are curved in their crossing portions.

6. An LCD comprising:

- a plurality of signal lines formed at constant intervals;

first and second common electrodes arranged at constant intervals in a vertical direction to the signal lines;

a plurality of first and second electrostatic protecting circuits arranged at both sides of the first common electrode between first and second signal lines of odd numbered pairs in adjacent first and second signal lines in pairs to be respectively connected with a pair of the first signal line and the first common electrode and a pair of the second signal line and the first common electrode;

and

a plurality of third and fourth electrostatic protecting circuits arranged at both sides of the second common electrode between the first and second signal lines of even numbered pairs to be respectively connected with a pair of the first signal line and the second common electrode and a pair of the second signal line and the second common electrode.

7. The LCD of claim 6, wherein the first and second electrostatic protecting circuits are arranged to cross the third and fourth electrostatic protecting circuits.

8. The LCD of claim 6, wherein the respective signal lines are curved in portions where a pair of the first and second electrostatic protecting circuits and a pair of the third and fourth electrostatic protecting circuits are arranged.

9. The LCD of claim 6, wherein the first common electrode includes two first and second lines, the first line being connected with the respective first electrostatic protecting circuit and the second line being connected with the respective second electrostatic protecting circuit.

10. The LCD of claim 6, wherein the second common electrode includes two first and second lines, the first line being connected

with the respective third electrostatic protecting circuit and the second line being connected with the respective fourth electrostatic protecting circuit.